

## THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:  
GERBER, MARK A., et al.

Application No.: 10/008800

Filed: 11-08-2001

Docket No.: SC11588TK

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Patent No.: 6916682 B2

Issue Date: 07-12-2005

Examiner: Michael Lebentritt

Group Art Unit: 2812

Title: SEMICONDUCTOR PACKAGE DEVICE FOR USE WITH MULTIPLE INTEGRATED CIRCUITS IN A STACKED CONFIGURATION AND METHOD OF FORMATION AND TESTING

## Certificate of Submission

I hereby certify that this correspondence is being submitted to the USPTO, Alexandria, VA.

- ☐ Addressed per C.F.R. § 1.1(a) and deposited with the United States Postal Service with sufficient postage as first class mail.
- ☐ Facsimile transmitted in accordance with C.F.R. §1.6(d).
- ☒ Submitted electronically via EFS in accordance with "Legal Framework for EFS Web".

February 20, 2008  
Date of Submission

Teresa Shephard  
Signature

Teresa Shephard  
Printed Name of Person Signing Certificate

Commissioner for Patents  
Alexandria, VA 22313

SUBMISSION OF CERTIFICATE OF CORRECTION

Dear Commissioner:

Enclosed is a Certificate of Correction listing error(s) in the subject patent. Please enter these corrections. Since the errors appear to be on the part of the United States Patent Office, there should be no charge.

2/19/08  
Date

Respectfully submitted,

Susan C Hill  
HILL, SUSAN C.

Attorney for Applicant(s)

Registration No. 35896

Telephone No. (512) 996-6839

Facsimile No. (512) 996-6854

**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION**

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PATENT NO.: 6916682 B2  
APPLICATION NO: 10/008800  
DATE: 11-08-2001  
FIRST NAMED INVENTOR: GERBER, MARK A.

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In Column 8, Line 9, Claim No. 16:

Change "material" to —material,—

In Column 8, Line 33, Claim No. 17:

Change "second pads." to—second pads,—

MAILING ADDRESS OF SENDER (Please do not use customer number below)

Freescale Semiconductor, Inc.  
Law Department  
7700 West Parmer Lane PL02  
Austin, TX 78729